Code No: RR220501 | RR | SET-1

B.Tech II Year - II Semester Examinations, April/May-2012
COMPUTER ORGANIZATION
(Common to Computer Science & Engineering, Information Technology)

Time: 3 hours **Max. Marks: 80**

Answer any five questions
All questions carry equal marks

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1.a) Explain the concept of Von Neumann machine.
b) Describe floating point representations. [16]

2.a) Discuss Booth's algorithm with illustrations.
b) Write about 1's complement and 2's complement representations. [16]

3.a) Discuss various addressing modes.
b) Explain register organization of Pentium processor. [16]

4.a) Compare and contrast RISC with CISC.
b) Describe instruction formats of PowerPC. [16]

5.a) Give a note on ferrite core memory.
b) What is virtual memory? Explain address translations. [16]

6.a) Explain working methodology of inkjet printer.
b) Discuss interrupt driven I/O. [16]

7. Explain microprogramming. Discuss the significance of vertical instruction formats for microprogramming. [16]

8. Discuss the problems in parallel processing. Explain briefly the mechanisms to overcome these problems. [16]

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Code No: RR220501 | RR | SET-2

B.Tech II Year - II Semester Examinations, April/May-2012
COMPUTER ORGANIZATION
(Common to Computer Science & Engineering, Information Technology)
Time: 3 hours **Max. Marks: 80**

Answer any five questions
All questions carry equal marks
- - -

1.a) Discuss various addressing modes.
b) Explain register organization of Pentium processor. [16]

2.a) Compare and contrast RISC with CISC.
b) Describe instruction formats of PowerPC. [16]

3.a) Give a note on ferrite core memory.
b) What is virtual memory? Explain address translations. [16]

4.a) Explain working methodology of inkjet printer.
b) Discuss interrupt driven I/O. [16]

5. Explain microprogramming. Discuss the significance of vertical instruction formats for microprogramming. [16]

6. Discuss the problems in parallel processing. Explain briefly the mechanisms to overcome these problems. [16]

7.a) Explain the concept of Von Neumann machine.
b) Describe floating point representations. [16]

8.a) Discuss Booth's algorithm with illustrations.
b) Write about 1's complement and 2's complement representations. [16]

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Code No: RR220501 | RR | SET-3

B.Tech II Year - II Semester Examinations, April/May-2012
COMPUTER ORGANIZATION
(Common to Computer Science & Engineering, Information Technology)
Time: 3 hours **Max. Marks: 80**

Answer any five questions
All questions carry equal marks
- - -

1.a) Give a note on ferrite core memory.
b) What is virtual memory? Explain address translations. [16]

2.a) Explain working methodology of inkjet printer.
b) Discuss interrupt driven I/O. [16]

3. Explain microprogramming. Discuss the significance of vertical instruction formats for microprogramming. [16]

4. Discuss the problems in parallel processing. Explain briefly the mechanisms to overcome these problems. [16]

5.a) Explain the concept of Von Neumann machine.
b) Describe floating point representations. [16]

6.a) Discuss Booth's algorithm with illustrations.
b) Write about 1's complement and 2's complement representations. [16]

7.a) Discuss various addressing modes.
b) Explain register organization of Pentium processor. [16]

8.a) Compare and contrast RISC with CISC.
b) Describe instruction formats of PowerPC. [16]

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Code No: RR220501 | RR | SET-4

B.Tech II Year - II Semester Examinations, April/May-2012
COMPUTER ORGANIZATION
(Common to Computer Science & Engineering, Information Technology)
Time: 3 hours **Max. Marks: 80**

Answer any five questions
All questions carry equal marks
- - -

1. Explain microprogramming. Discuss the significance of vertical instruction formats for microprogramming. [16]

2. Discuss the problems in parallel processing. Explain briefly the mechanisms to overcome these problems. [16]

3.a) Explain the concept of Von Neumann machine.
b) Describe floating point representations. [16]

4.a) Discuss Booth's algorithm with illustrations.
b) Write about 1's complement and 2's complement representations. [16]

5.a) Discuss various addressing modes.
b) Explain register organization of Pentium processor. [16]

6.a) Compare and contrast RISC with CISC.
b) Describe instruction formats of PowerPC. [16]

7.a) Give a note on ferrite core memory.
b) What is virtual memory? Explain address translations. [16]

8.a) Explain working methodology of inkjet printer.
b) Discuss interrupt driven I/O. [16]

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