R SR SF

Code No: 136AQ

R16

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD B. Tech III Year II Semester Examinations, December - 2019 COMPILER DESIGN

(Common to CSE, IT)			
8	Time	:3 hours SR SR SMax. Marks	s:75 R
	Note:	This question paper contains two parts A and B. Part A is compulsory which carries 25 marks. Answer all questions in Part A. Paconsists of 5 Units. Answer any one full question from each unit. Each question can 10 marks and may have a, b, c as sub questions.	art B
8	R	SR SR SR SR 25 Ma	Onrks)
8	1.a) b) c) d) e) f) g) h) i)	Differentiate between pass one and pass two of a compiler. Define context free grammar. What do you mean by ambiguous grammar? Give example. Convert the expression a= b*-c + b*-c into three Address statements. What is an attribute? Explain different types of attribute? List the different storage allocation strategies. What are the sub problems in register allocation strategies? Give the applications of DAG.	2] 3] 2] 3] 2] 2] 3] 2]
	j)	What is the role of flow graph optimizing a compiler?	3]
8	2.	PART - B With a neat diagram explain the phases of a compiler in detail.	11ks)
	3.	OR	[10]
8	4.	Test whether the grammar is LL(1) or not and construct a predictive parsing table for SiEtS iEtSeS a E b OR	
	6.		[10]
81	7. 8.	Write the three address code sequence for the d:=(a-b)+(a-c)+(a-c). Give example to explain peephole optimization technique in detail. OR	
	9.	W	[10]
8	10.	With an example explain common sub expression and dead code elimination method in detail. What is DAG and flowgraph? Explain their role in compilation process.	nods [10]