Code No: 133AB

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

		B.Tech II Year I Semester Examinations, May/Jun	ne - 2019	
		ANALOG ELECTRONICS		
		(Common to ECE, ETM)	Max. Marks: 75	
Same !	Time:	3 Hours	Max. Marks. 75	
	Note:	This question paper contains two parts A and B.		
	Note.	Port A is compulsory which carries 25 marks. Answer all quest	ions in Part A.	
		Part B consists of 5 Units. Answer any one full ques	stion from each i	unit.
anto-		Each question carries 10 marks and may have a, b, c as sub que	estions.	
		XUXXLX XHC XHC		
		CALL PART-A	(25	Marks)
			(25	Marks) [2]
	1.a)	What are the types of distortion in amplifiers?		[3]
s •	b)	Write the difference between cascade and cascode amplifiers?		[2]
	c)	Define Gain Bandwidth Product.		[3]
Same hame	d)	What are the elements in the hybrid π model? Distinguish between enhance mode and depletion mode of MC	SFET	[2]
$\geq \langle \perp \rangle$	e)	Distinguish between ennance mode and depiction mode of the		[3]
A	(f)	What is folded Cascode amplifier? What is meant by positive and negative feedback?		[2]
	g)	What are the conditions for oscillation?		[3]
	h)	Compare class A and class B amplifier.		[2]
	i) j)	Define Q-Factor in tuned amplifiers.		[3]
	J)	Define & Luctor in thirds		
/ T	·	OPART-B	- O (1)	
24 -	1	KK KK SK OK		Marks)
Samuel 3	2.	Draw the h-parameter equivalent circuit for a typical com	mon emitter ampi	mer and
		derive expression for A_i , A_{V_i} , R_i and R_0 .		[10]
		OR		
	3.a)	For any transistor amplifier, Prove that $R_i = (h_i/1 - h_r A_v)$ Draw the circuit diagram of RC coupled amplifier. Exp	lain the operation	and its
	b)		idili tile operation	[5+5]
	`}	frequency response.		
		Derive the expression for the CE short circuit gain Ai as a	function of frequer	ncy using
1	4.	hybrid – π model.		[10]
		OR	20	
	5.a)	In hybrid 'Pi' model of a transistor at high frequencies, show	v that the g _m is pro	portional
	,	to collector current.		
	b)	Mention important characteristics of CE amplifier.		[5+5]
)		l - Laukamaan	mont type
	(6.a)	With the help of a neat diagram explain the operation of an	n-channel enhancel	nent type
		MOSFET.		[5+5]
	b)	Explain how you set a Q point in a self- biased JFET.		
		OR		
	7.a)	Derive the relation between u and g_m of JFET amplifier.	4V find:	
garage garage	b)	A JFET has a drain current of 6mA. If $I_{DSS} = 12$ mA and $V_P = 12$ mA	(5)	$()$ Γ
).	(i) V_{GS} (ii) For an n-channel amplifier FET I_{DSS} =5.8 mA. V_P =-3V and	V_{GS} =-2 V find I_D a	ınd g _m .
	1	TITLOL WILLIAM HISTORY AND AS	Some 1 1	[5+5]

